



1A, 50V - 1000V Fast Surface Mount Rectifier

FEATURES

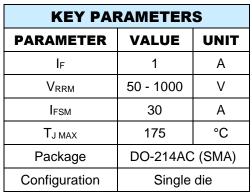
- Glass passivated chip junction
- Ideal for automated placement
- Fast switching for high efficiency
- Moisture sensitivity level: level 1, per J-STD-020
- RoHS Compliant
- Halogen-free

APPLICATIONS

- DC to DC converter
- Switching mode converters and inverters
- Lighting application
- Snubber
- General purpose

MECHANICAL DATA

- Case: DO-214AC (SMA)
- Molding compound meets UL 94V-0 flammability rating
- Terminal: Matte tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 2 whisker test
- Polarity: Indicated by cathode band
- Weight: 0.060g (approximately)











DO-214AC (SMA)



PARAMETER	SYMBOL	RS1A	RS1B	RS1D	RS1G	RS1J	RS1K	RS1M	UNIT
Marking code on the device		RS1A	RS1B	RS1D	RS1G	RS1J	RS1K	RS1M	
Repetitive peak reverse voltage	V _{RRM}	50	100	200	400	600	800	1000	V
Reverse voltage, total rms value	V _{R(RMS)}	35	70	140	280	420	560	700	V
Forward current	l _F				1				Α
Peak forward surge current, 8.3ms single half sine wave superimposed on rated load	IFSM	30						А	
Junction temperature	TJ	- 55 to +175					°C		
Storage temperature	T_{STG}	- 55 to +175				°C			

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THERMAL PERFORMANCE						
PARAMETER	SYMBOL	ТҮР	UNIT			
Junction-to-ambient thermal resistance	Reja	105	°C/W			
Junction-to-lead thermal resistance	ReJL	30	°C/W			

PARAMETER		CONDITIONS	SYMBOL	TYP	MAX	UNIT
Forward voltage ⁽¹⁾		I _F = 1A, T _J = 25°C	VF	-	1.3	V
Reverse current @ rated V _R ⁽²⁾		T _J = 25°C		-	5	μΑ
		T _J = 125°C	- I _R	-	50	μΑ
Junction capacitance		1MHz, V _R = 4.0V	Сл	10	-	pF
Reverse recovery time	RS1A RS1B RS1D RS1G	I _F = 0.5A, I _R = 1.0A, I _{rr} = 0.25A	t _{rr}	-	150	ns
	RS1J			-	250	ns
	RS1K RS1M			-	500	ns

Notes:

- 1. Pulse test with PW = 0.3ms
- 2. Pulse test with PW = 30ms

ORDERING INFORMATION					
ORDERING CODE ⁽¹⁾	PACKAGE	PACKING			
RS1x	DO-214AC (SMA)	7,500 / Tape & Reel			

Notes:

1. "x" defines voltage from 50V(RS1A) to 1000V(RS1M)

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CHARACTERISTICS CURVES

(T_A = 25°C unless otherwise noted)

Fig.1 Forward Current Derating Curve

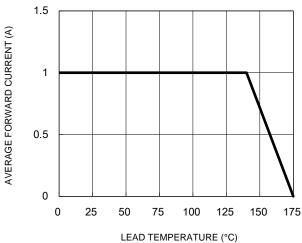
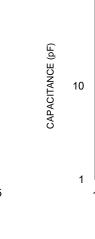


Fig.3 Typical Reverse Characteristics



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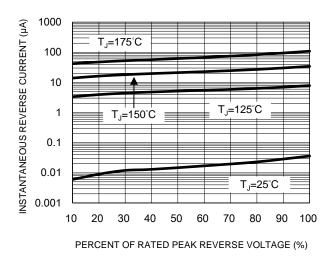
f=1.0MHz Vsig=50mVp-p

Fig.4 Typical Forward Characteristics

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REVERSE VOLTAGE (V)

Fig.2 Typical Junction Capacitance



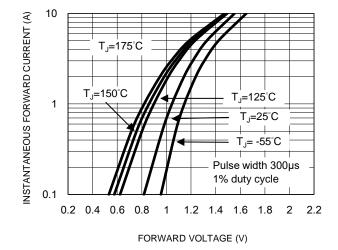
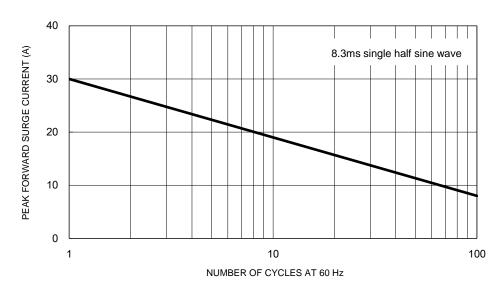


Fig.5 Maximum Non-Repetitive Forward Surge Current



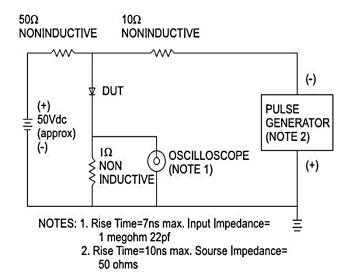


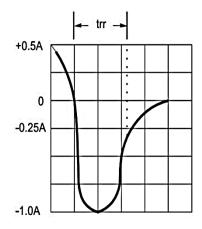


CHARACTERISTICS CURVES

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$

Fig.6 Reverse Recovery Time Characteristic and Test Circuit Diagram

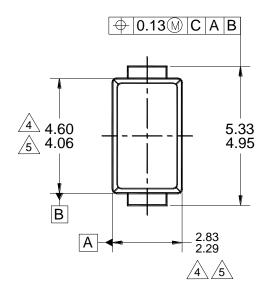


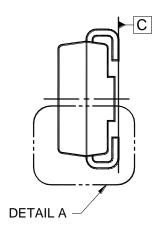


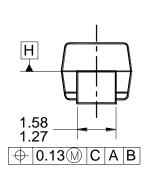


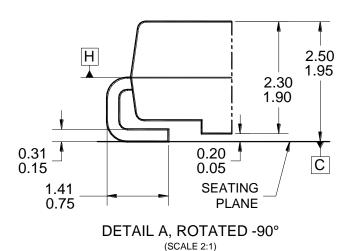
PACKAGE OUTLINE DIMENSIONS

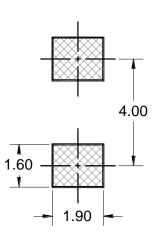
DO-214AC (SMA)



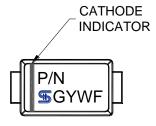








SUGGESTED PAD LAYOUT



MARKING DIAGRAM

P/N = MARKING CODE

G = GREEN COMPOUND

YW = DATE CODE

F = FACTORY CODE

NOTES: UNLESS OTHERWISE SPECIFIED

- 1. ALL DIMENSIONS ARE IN MILLIMETERS.
- 2. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994.
- 3. PACKAGE OUTLINE REFERENCE: JEDEC DO-214, VARIATION AC, ISSUE D.



MOLDED PLASTIC BODY DIMENSIONS DO NOT INCLUDE MOLD FLASH.



6. DWG NO. REF: HQ2SD07-DO214SMC-034 REV A.

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